

(OC-8 改造説明書)

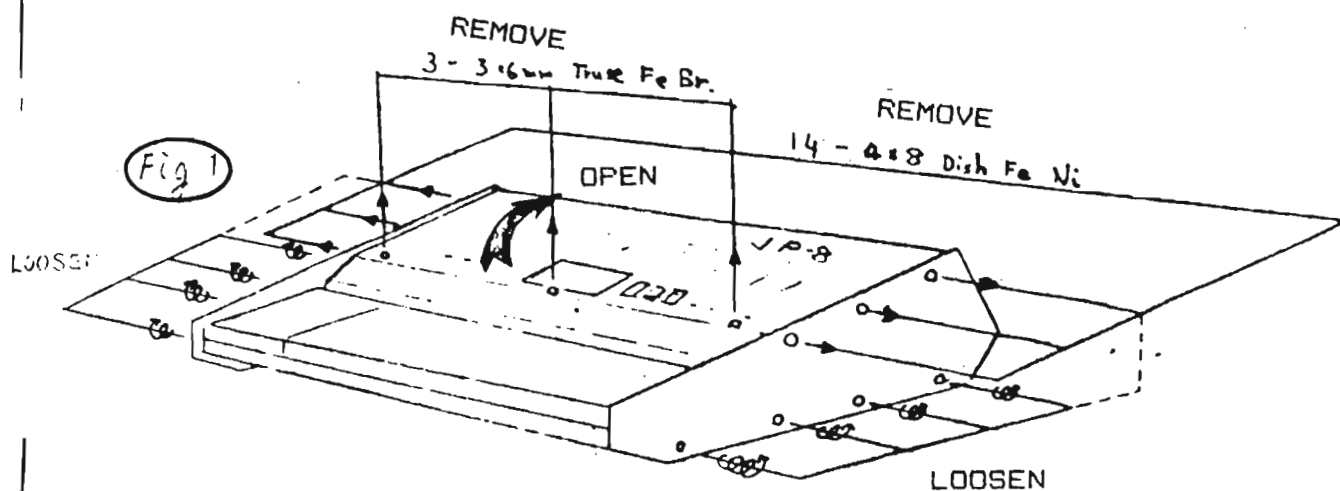
(OC-8 MODIFICATION PROCEDURE)

 **Roland**

(NEW OP-8 接続についてのJP-8変更手順)

JP-8 CHANGING PROCEDURE

1: DISASSEMBLE

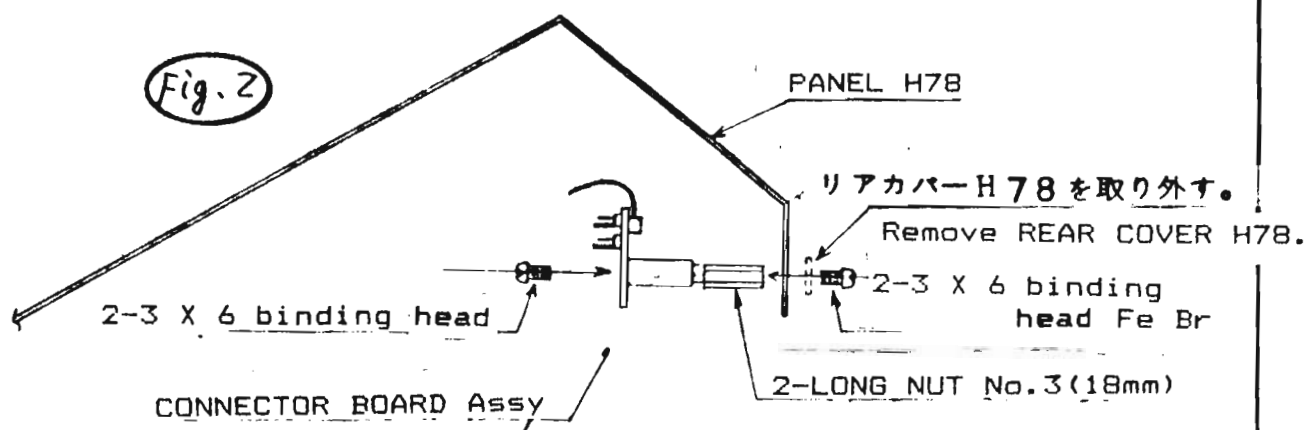


注) パネル開閉時、側板内側にキズがつきやすいので注意して下さい。

NOTE) Care should be taken not to scratch the side panels.

2: パネル後面のリアカバーH78を2本のビス(またはナイロンリベット)を取り外しFig.2の様にCONNECTOR BOARD Assyを取り付ける。

2: Remove REAR COVER H78 and fix CONNECTOR BOARD Assy as Fig.2.



(PANEL BOARD Aへのワイヤソケットの取付方)
 (SOLDERING W - F CONNECTOR)

1 Lot (SERIAL No. 030100) - 5 Lot (SERIAL No. 090599)
 ETCH MASH. 052H271

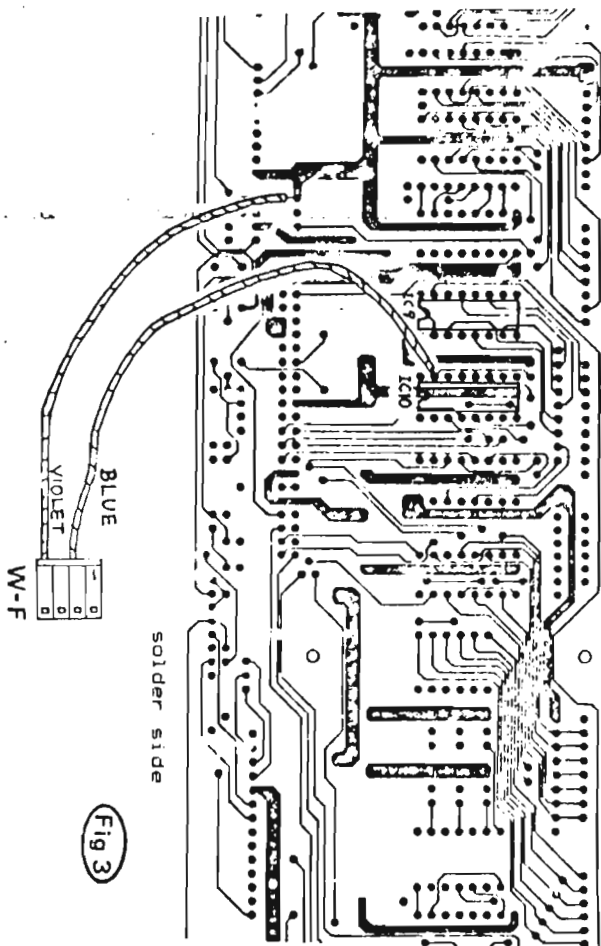


Fig 3

3: PANEL A BOARDにFig.3, Fig.4のワイヤソケットを半田付する。

3: Solder W - F to PANEL BOARD A as Fig.3 or Fig.4 according to JF-8 serial number.

6 Lot (SERIAL No. 090600 -)
 ETCH MASH. 052H271

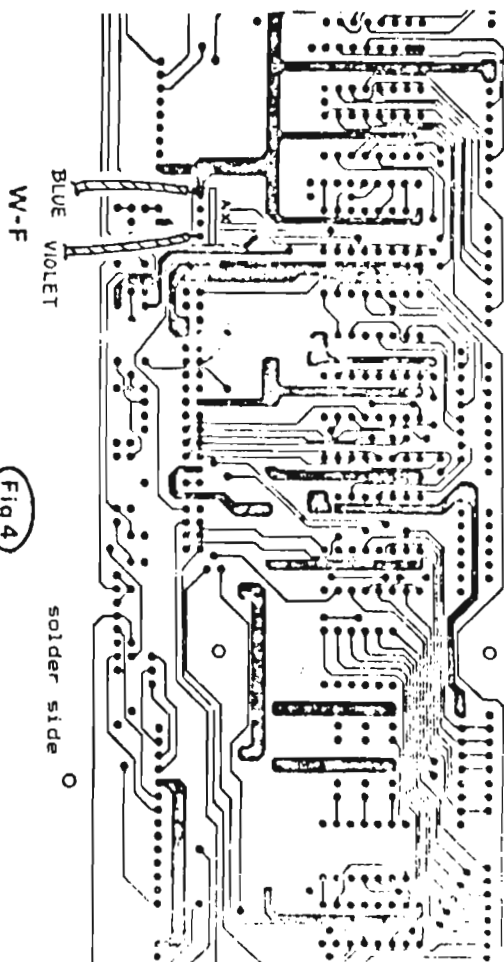


Fig 4

A X	4 3 2 1
EXT	W-F
D	

131021

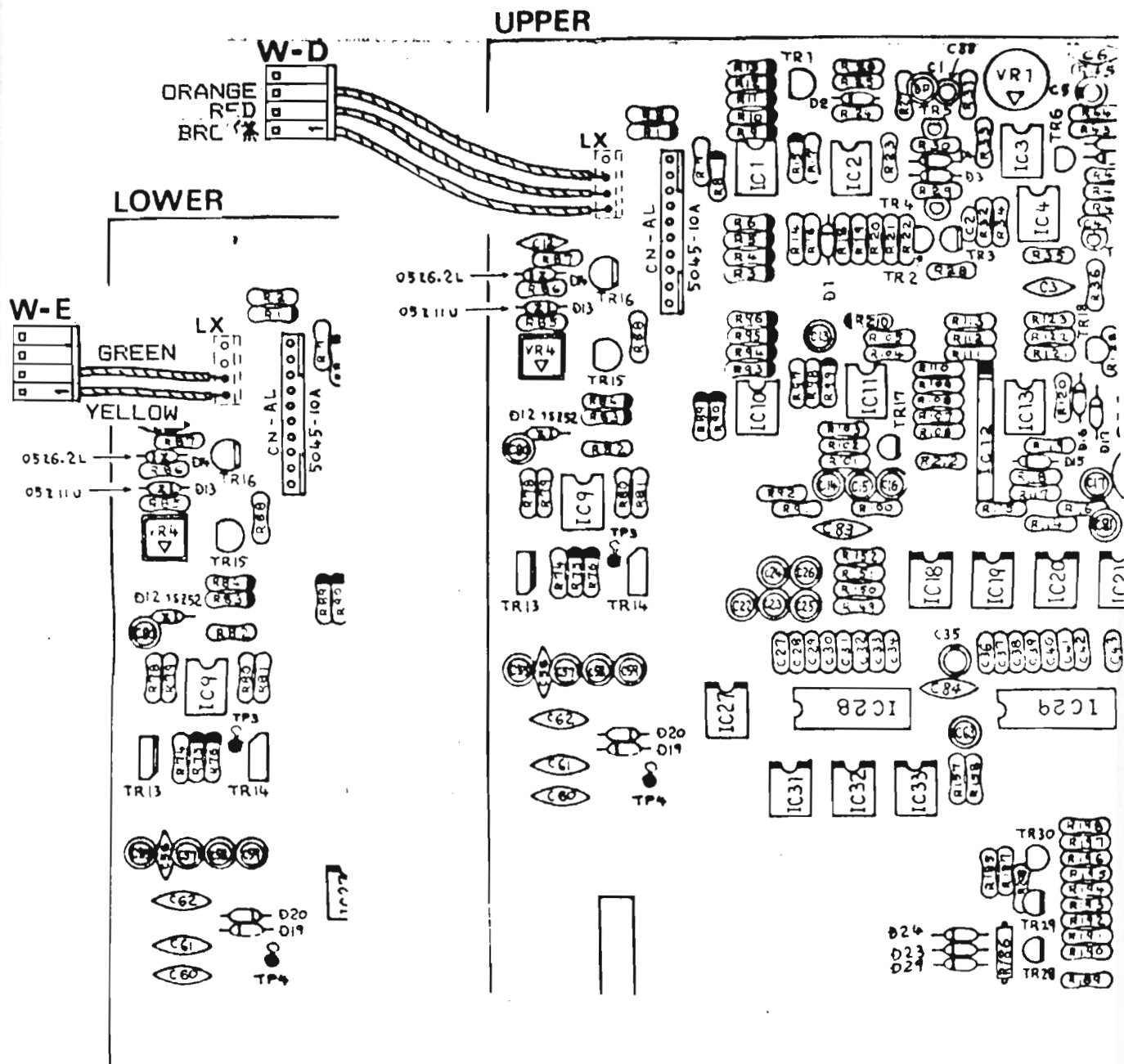
05 03

W-

(MOD-CON へのファイヤリングの続き方)

[SOLDERING W - D AND W - E]

Fig 5



4: MOD-CON BOARDの
UPPERとLOWERにF1 g・5
の様にファイヤリングD、Eを
半田付する。

GND	4	L X
GND	3	
VCF CV	2	
VCA CV	1	

4: Solder W - D
and W - E to
MOD-CON BOARD
as Fig.5.

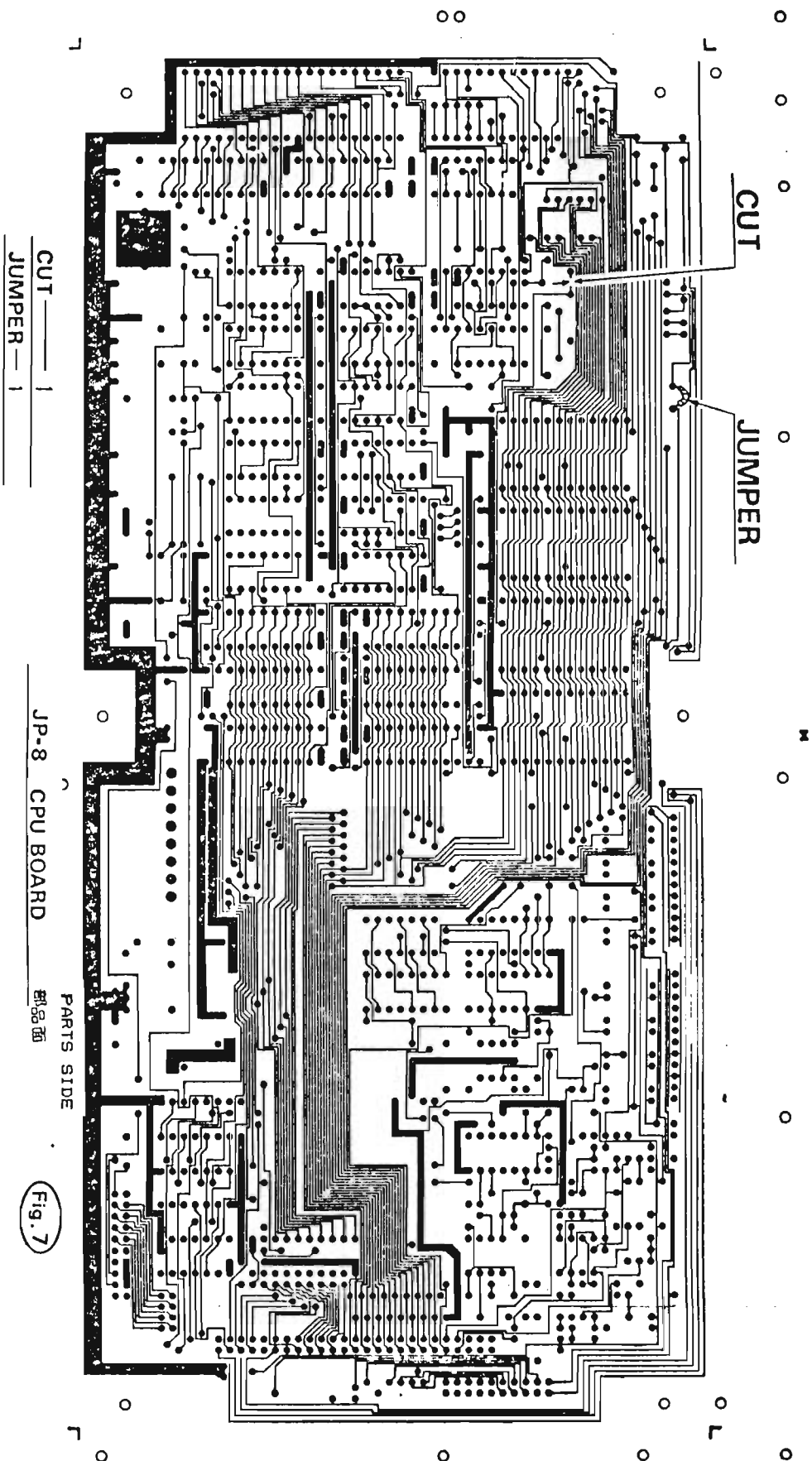
[SOLDERING W - D AND W - E]

GND	4	L X
GND	3	
VCF CV	2	
VCA CV	1	

4: Solder W - D
and W - E to
MOD-CON BOARD
as Fig. 5.

(JP-8 CPU BOARD 変更図) 2-1
部 品 面

[CPU BOARD MODIFICATION] 2-1
PARTS SIDE



8: CPU BOARDを外しFig. 7の様に部品面のパターンを変更する。

8: Modify CPU BOARD parts side as Fig. 7.

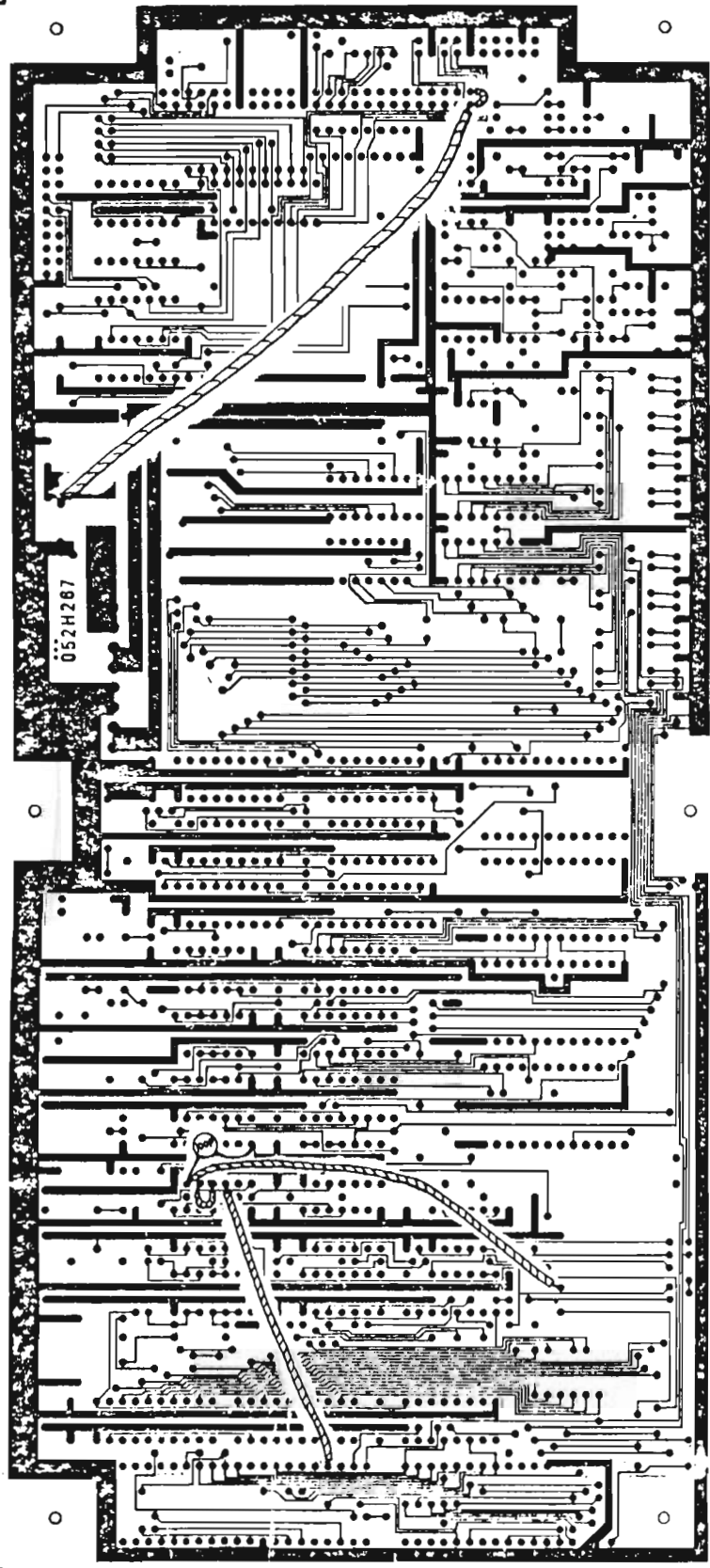


Fig. 8

JP-8 CPU BOARD
SOLDER SIDE
半田面

JUMPER — 5
CAPASITOR (100P) — 1

- 9: Fig. 8の様にCPU BOARD半田面の変更を付図の材料を必要な長さに切つて5ヶ所ジャンパーし、セラミックコンデンサー100PFを半田付する。
- 9: Cut the attached wire at appropriate length and solder them with 100PF ceramic capacitor as Fig. 8.

JP-8 CPU BOARD

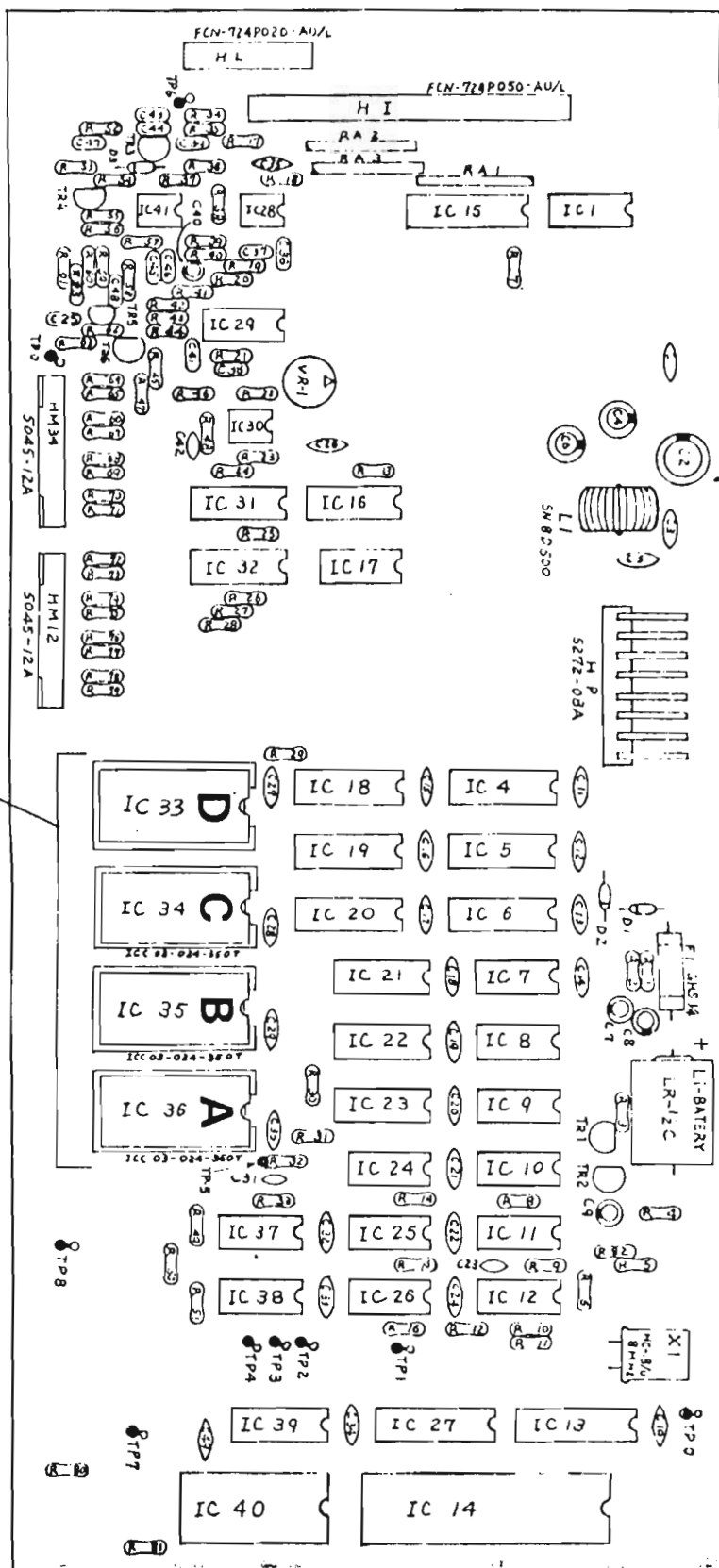


Fig.9

10: IC33~IC36のμPD2716を付録のPROMに交換する。
10: Remove three PROMs and mount new PROM set.

CONNECTOR BOARD

11: Fix CPU BOARD and connect HL and HI connector.

Fix INTERFACE BOARD and OPTION BOARD with HOLDER H134.

Connect HI, IM and AI connector (see Fig.11).

12: Connect 20P FLAT CABLE from OPTIN BOARD to CONNECTOR BOARD as Fig.10.

13: Fix top panel and side panels.

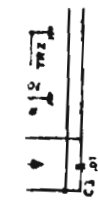
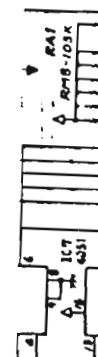
NOTE) Removed FLAT CABLE H126 is not used after this modification.

11: CPU BOARDの変更があれば、BOARDを元に戻し、Fig.11の様にOPTION BOARDをホルダーH134にビスで固定しOPTION BOARDからの50PFLAT CABLEをCPU及びINTERFACE BOARDのHIコネクタに接続してIMコネクタ及びAIコネクタも元に戻す。

12: OPTION BOARDからの20PFLAT CABLEはFig.10の様に接続する。

13: 基板を固定しパネルを開けてはります。尚、外したフラットケーブル(053H126)は変更後には使用しません。

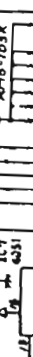
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IC9 Y213
1558 25B605



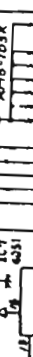
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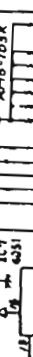
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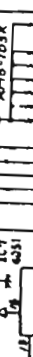
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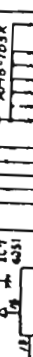
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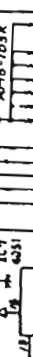
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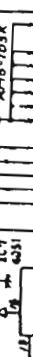
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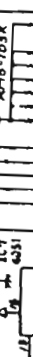
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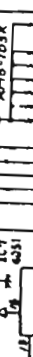
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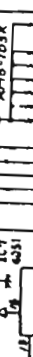
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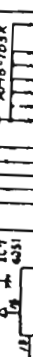
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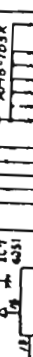
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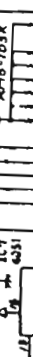
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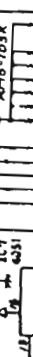
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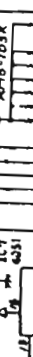
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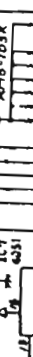
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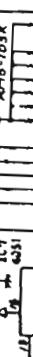
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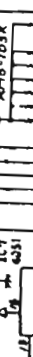
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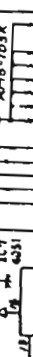
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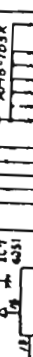
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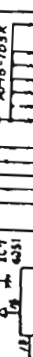
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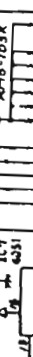
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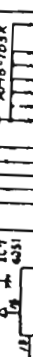
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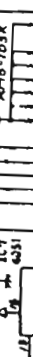
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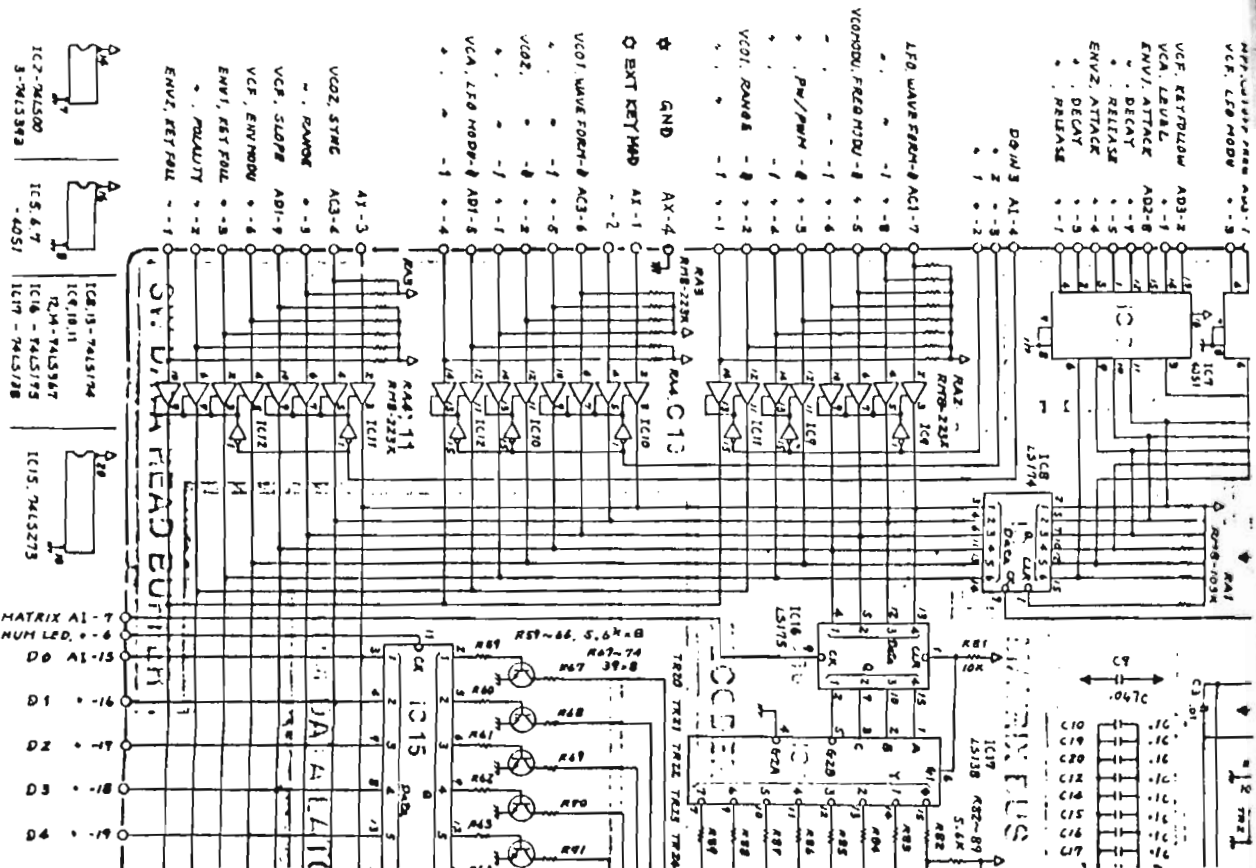


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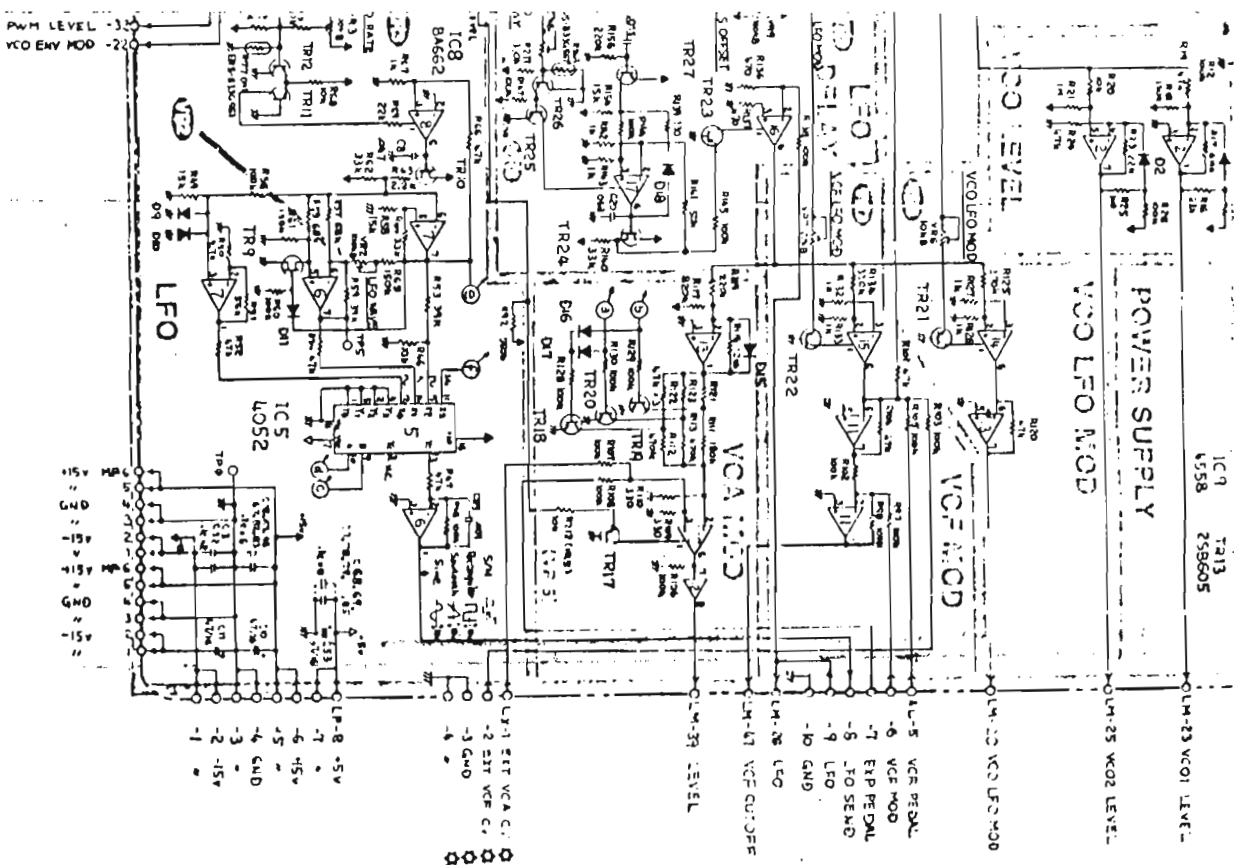
PANEL A BOARD

FROM CONNECTOR BOARD

Highest Ref Des are RM, C21, D14, TR36, IC17, RAS



MOD - CON BOARD



OC-8 PARTS LIST

* OPTION BOARD Assy (OPH170) -----	1 Pcs
* CONNECTOR BOARD Assy (OPH169) -----	1 Pcs
* IC uPD2716 (A,B,C,D) -----	4 Pcs
* HOLDER H134 -----	1 Pcs
* WIRING D -----	1 Pcs
* WIRING E -----	1 Pcs
* WIRING F -----	1 Pcs
* CAPASITOR (100PF) -----	1 Pcs
* WIRE (350mm) -----	1 Pcs
* LONG NUT #3 (18mm) -----	2 Pcs
* SCREWS (BIND 3 x 6mm Fe Br) -----	6 Pcs

Log	Function	Colour Code
1	Busy	w/R ₁
2	Data	Grey 1
3	Clock Rec in	Orange 1
4	GND	w/Blue 1
5	Busy	Orange / R ₂
6	Data	Pink / R ₁
7	Clock Tx	Yellow / R ₁
8	Unreg.	—
9	VCA lower	Yellow / Blue 2
10	VCA upper	Yellow / Red 2
11	VCF lower	Pink 1B2
12	VCF upper	Pink / R ₂
13	VCO 2	w / B ₂
14	VCO 1	w / R ₂
15		

NEW PCB

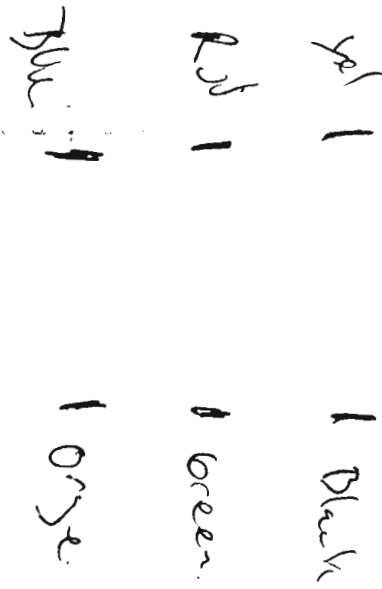
- 1 w/R1
- 2 Grey 1
- 3 Orange 1
- 4 w/Blac 1
- 5 Orange/R2
- 6 Pink/R1
- 7 Yel Red 1

8

- 9 Yel / Blk 2
- 10 Yel R2 2
- 11 Pink / B / 2
- 12 Pink R / 2
- 13 W B / 2
- 14 W R / 2

PCB.

- 1 Busv
- 2 Dat
- 3 clock Rcv
- 4 GND (GND)
- 5 Busv
- 6 Data
- 7 clock Tx
- 8 w-reg
- 9 VCA Low
- 10 VCA Upper
- 11 VCF Low
- 12 VCF VCF
- 13 VCF 2
- 14 VCF 1
- 15



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